



Image

Attorney's Docket No.: 07977-226002 / US3548D1

B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani

Art Unit : 1765

Serial No. : 09/596,755

Examiner : Robert Kunemund

Filed : June 15, 2000

Title : MANUFACTURING METHOD OF SEMICONDUCTOR AND  
MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL REPLY TO OFFICE ACTION

Prior to examination and supplemental to the amendment filed on October 23, 2003,  
please amend the application as indicated on the following pages.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2  
of this paper.

**Remarks/Arguments** begin on page 10 of this paper.